Message from the Symposium Chair

Welcome to Portland, the industrial and educational capital of Oregon, a site of the 30th International Symposium on Multiple-Valued Logic. Portland, the “City of Roses”, is located at the confluence of the Columbia and Willamette Rivers. Portland parks include the world-famous Rose Gardens and the exquisite Japanese Gardens (supposedly, the best outside Japan). Downtown Portland offers museums, fine shopping, running and walking paths and a wide variety of restaurants. Within a short drive are the Columbia River Gorge’s numerous hiking trails and splendid waterfalls. An hour west are the Oregon Coast’s easily accessed beaches, to the east is the splendid Mt. Hood and to the north the Mount St. Helens volcano.

Portland is also a center of high technology; with companies such as Intel, Tektronix, Mentor Graphics, Synopsys, Cadence, Electro-Scientific Industries, LSI Logic, Sharp, Cypress and many others. Many new ideas in computer architecture, programmable devices, simulation, verification, test, logic synthesis, circuits, and other areas related to this symposium, have been created in Oregon’s Silicon Forest. Remember, the microprocessor chips used in most world computers are designed and fabricated here.

It is the first time that the symposium is being held in Northwest region of the United States. Like in the past years, researchers from different areas will discuss the latest results in the field of multiple-valued logic. The symposium is co-sponsored by the Portland State University, and the IEEE Computer Society. I am grateful for the generous financial support from the University and several industrial companies.

I wish to express my gratitude to the Symposium Committee for their hard work in preparing this event. Prof. Dan Simovici served as a Program Chair for Americas, Prof. Yutaka Hata served as a Program Chair for Asia and Australia, and Prof. Svetlana Yanushkevich served as a Program Chair for Europe and Africa. I would like to thank them for organizing an excellent program. ISMVL 2000 has been the joint effort of many people. I would like to thank especially Prof. Malgorzata Chrzanowska-Jeske (Financial Chair), Prof. Xiayou Song (Publication Chair), and Prof. W. Robert Daasch (Local Arrangement Chair).

This year, the symposium is enhanced not only by the traditional ULSI workshop, but also for the first time by the Third Oregon Symposium on Logic, Design and Learning, which investigates applications of logic outside traditional circuit design. We are also happy having Dr. Wolf, the famous researcher in cloning, to be a banquet speaker, and having a record number of seven excellent invited speakers, all top researchers in their specialization areas.

Finally, I would like to wish all participants to have a scientifically interesting and turistically exciting time in Portland and Oregon. I hope that most of you will return back to Pacific Northwest; a miraculous mixture of the highest technology and most natural beauty. I hope also that you will feel encouraged to participate in ISMVL’01 in Poland, the motherland of both Jan Lukasiewicz and Emil Post.

Marek Perkowski
Symposium Chair