Performance modeling in the design process

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ABSTRACT

Performance modeling and analysis of computer systems are often ignored during the project design phase in favor of other techniques collectively known as structured design or software engineering. We describe benefits that can result from including performance analysis as an integral part of the design process. Several different goals, time frames, and roles played by performance analysis during system design are illustrated by three case studies of current projects at Los Alamos.
INTRODUCTION

In the past decade, since the ideas collectively referred to as *structured programming* and *structured design* have gained acceptance, the conventional wisdom has become that one should ignore performance considerations in the design phase of a computer system project in favor of modifiability, maintainability, and correctness. This doctrine assumes that one can always "tune" the system to meet performance criteria after it is built. If one is astute enough in design choices so that the first running version of the system comes within, say, an order of magnitude of the performance goals, these goals often can be met by relatively simple modifications; in this case the attention paid during design to understandable structure and modifiability will be rewarded. But performance is a result of interactions among many elements of software, hardware, and environment, and sometimes these interactions are counterintuitive. It is not uncommon for systems to fall so far short of performance goals that only fundamental, and therefore very expensive, changes will serve.

It is our experience that the chances of such catastrophes can be reduced by applying modeling techniques during the system design process in such a way that none of the advantages of structured design need be sacrificed.

The models we refer to in this paper may be analytic or simulation; in some cases the simpler methods of operational analysis are adequate. The point is that sufficient data should be collected, and sufficient analysis done on them, to give some assurance that the performance goals of the system being designed will be met. The kinds of data that must be gathered include hardware and software characteristics of the components of the system; measures of the behavior of the environment in which the system will run, including workload and competing systems; and even the ways in which the system being designed will alter the existing environment. Because most design projects have deadlines, it is much more likely that sufficient performance analysis will be included if the installation already has models of the proposed system's environment, or at least has data collection facilities installed.

Differences in performance goals as well as in other design objectives imply that modeling will assume different roles, occupy different time frames, and require different information from one design project to the next. Three design projects with which we have been involved at Los Alamos illustrate some of these points.

COMPUTING AT LOS ALAMOS

At Los Alamos, the integrated computing network (ICN) allows all validated computer users at the laboratory access to almost any of the machines or services of the Central Computing Facility (CCF). Figure 1 is a schematic diagram of the ICN. (Dotted portions indicate future plans.) At the "front end" of the network (the right side of the diagram) over 1,350 terminals and remote entry stations are concentrated in stages to front-end switches (the SYNCS) so that traffic can be routed between any terminal and any worker computer. The worker computers include four Cray-1s, four CDC 7600s, two CDC Cyber-73s, and a CDC 6600. Each of the worker computers is connected to the file transport (FT) switches and so to the "back end" of the network (left side of the diagram). The FTs are the means by which workers can send files to each other and to the special-service nodes in the network. The special services provided at present by the network include an output station (PAGES) to which are attached a wide variety of printing and graphics devices, a mass storage and archive facility (CFS), a gateway that handles file traffic between workers and computers outside the ICN, and an integrated performance monitoring and batch job control station (FOCUS).

Although all types of computing are done at Los Alamos, most of the CPU hours on the large workers are spent executing large, long-running scientific programs. Many of these produce graphics output. Some users have a need to run programs larger and longer than even our present worker computers can handle.

THREE CASE STUDIES IN DESIGN

The distributed interactive graphics project

The goal of the distributed interactive graphics project is to improve user productivity by improving the performance of an existing interactive graphics system that runs on a large scientific computer (a Cray-1 or a CDC 7600). It is hoped that system responsiveness can be improved by adding an intelligent terminal or a larger minicomputer as a front end and by distributing the software between the two computers. The front end is intended to handle graphics-terminal or device interactions and drive the graphics screen. Design issues include the choice of minicomputer, the hardware and software constituting the link between the two computers, the distribution of the graphics software, and whether the distribution should be static or dynamic.

A simple model of this system might include CPU and memory on the two computers and simple links between the two computers and between the front end and the terminal. Input to this simple model would include the speed and size of the CPUs and memories and the bandwidth of each link. We would also need the CPU burst sizes and distributions and the
frequency and size of communications over each link for a given distribution of the software. All this information will probably be known or can be obtained by the system designers. This model can only give an order-of-magnitude estimate of performance and tell the designers whether a component of the system is an obvious bottleneck.

A more realistic model of this system would incorporate other information not so readily available to the application designers. The link between the two computers envisioned by the designers is actually the back end of the network depicted in Figure 1. Contention for network services, overhead in communication protocols, error rates, and buffer space within the network are all likely to reduce the effective bandwidth of the communications links. Contention for CPU and memory resources on the two computers will also alter the communication bandwidth and the rate at which the distributed application can execute. As the distribution of software between the two computers changes, the competition this system introduces into each of the two computer systems will change also. Quantitative knowledge about the effects of these factors will be available only if there has been an ongoing data collection effort on the mainframes and the network. There probably would not have been time or staff available to obtain the information for the design effort if it were not already being collected.

One way to get around the problem of missing information is to distribute a prototype application and measure the effective communication bandwidth and rates of CPU service. The designers did this, using a DEC VAX 11/780 as the front end. The initial performance results were discouraging. The designers concluded that the chosen division of software between the two computers was wrong. They also observed that the number of ways of splitting the application was too large for exhaustive trial and error. Yet the frequency and size of communications between each pair of modules was not known, and the model needed this information to predict performance for a given split of the software. The designers therefore invested the time to build distributing tools that automated the code conversion process, allowing them to move modules from one machine to another more easily. This effort turned their prototype into a flexible data collection tool for performance analysis, as well as providing the designers with useful insights.

The prototype multiprocessor project

The prototype multiprocessor project has as a short-term goal the production of a tool for evaluating various approaches to parallelizing certain classes of numerical com-
The goal of the network switch project is to design a network switch to replace the SEL 32/77 minicomputers that act as file transports in the back end of the ICN. The new switch will have more ports so it can be connected to more network nodes than each SEL can, and it will have hardware support for error detection so that the network can provide more reliability without software overhead. The performance of the current FTs is quite satisfactory, so improving performance is not a primary motivation for this project. Building a prototype of the proposed new switch is unnecessary, because the present FTs serve very well in that role.

It is desirable that the new FTs meet performance needs imposed by ever-increasing message traffic rates in the back end of the network as far into the future as possible. A relatively simple model of the proposed switch and its environment was constructed to investigate its performance under loads in excess of those observed at present. The data needed for this model included characteristics of the CPU, buffer memory, channels chosen, and current back-end network message rates. Once again, the fact that these message rates were already being collected made the modeling effort more practicable.

Merely increasing the present message rates with the same distribution of large versus small messages and with the same set of nodes currently comprising the network constituted a reasonable extrapolation of future workload for the FTs for two or three years. But predicting workload growth in any computing system more than a few years in the future is practically impossible because there are certain to be qualitative changes in the structure of the system as well as in the way people use it. We have discussed this problem in Alexander and Brice. 7 Our approach was to vary all the workload parameters over a wide range of values. In this way we were able to predict what loads the proposed system would handle and the characteristics of loads that may cause its performance to deteriorate.

MODELS VS. PROTOTYPES AS PERFORMANCE ANALYSIS TOOLS

In these three examples, we have seen both models and prototypes play differing roles in the design process. Obviously, prototypes serve many useful purposes in design, but we are interested here only in their uses for performance prediction and their relationship to models. Models and prototypes have different strengths and weaknesses as performance prediction tools.

Prototypes can provide order-of-magnitude performance information, especially if they can be installed in the actual environment in which the production system being designed is to run. But as performance predictors they have three major drawbacks.

1. It is difficult to extrapolate measured service rates of a prototype to the production system, because it is not possible to predict accurately how the different behavior of the production system will interact with its environment. The production system will presumably have memory size, computational needs, communication behavior, and interaction rates different from the prototype, and these will affect the environment as well as being affected by it. Models can incorporate the environment.

2. Although prototypes usually can be used to predict the effect on performance of simple changes in the system, they cannot do the same for changes in the environment.
For example, we know that the distributed graphics system will run faster if we are allowed to make one simple change in the scheduling algorithm of the operating system used on the large scientific computers, but this fact could be learned only from a model that incorporated the operating system.

3. Prototypes usually cannot be made as flexible as models; hence, prototypes cannot be used to predict the effect of fundamental design changes. This point requires further discussion.

Flexibility can sometimes be achieved in prototypes, but usually at higher cost than in models. Prototypes are not always inflexible, but it is unusual to spend so much time and effort in the computer system design process on a prototype. The multiprocessor prototype was consciously designed as a hardware simulator of a variety of multiprocessor architectures, and this capability makes it very like a model. One can imagine a continuum characterized by increasing cost and complexity as one moves from operational analysis through models and prototypes to the actual production system. The design process is characterized by making choices among alternatives; typically one can try different alternatives more quickly and much more cheaply with a model than by actually implementing them. There is a subjective cost/benefit function that applies to the choice between trusting the results of a model and implementing a prototype. The cost of implementing a prototype is usually more easily justified in extreme situations, such as designing with new technologies (including new software technologies) or in completely unfamiliar situations.

Modeling as part of the design process produces benefits besides performance prediction. Because modeling is a relatively quick method of "implementing" a design, issues that normally come up only during implementation sometimes arise much earlier. Ambiguities in the specifications may be noticed early, and if these would have necessitated redesign, time and money can be saved. Beyond merely predicting performance, modeling can specify the performance levels that individual components will have to achieve for the whole system to meet its performance goals. Modeling can also help explore the behavior of the system under a variety of workloads or other external conditions.

Although modeling is an art requiring some expertise, it is not nearly so difficult as it used to be. A number of commercial packages and languages are available to support analytic and simulation modeling of computer systems. It is not unreasonable to develop models and in-house modeling capabilities with the aid of these tools, and the investment can pay repeated dividends.

There are, of course, difficulties in integrating modeling into the design process. It is particularly difficult to analyze the performance of software that has not been written. Although the work of Smith and Browne is promising, much remains to be done. We do not imply that performance modeling as part of design always works or can answer all questions, only that it has proved useful in our experience.

CONCLUSIONS

Here, in capsule form, are some lessons we have learned in trying to integrate performance modeling into the design process:

- Performance modeling should play a central role in system design; ignore it at your peril.
- The role of performance modeling is not the same in all design projects. Clearly specify your performance goals and what factors will affect performance; then try to model those factors.
- Obtaining the data for the models can be a major problem; ongoing measurement projects are always worthwhile.
- Prototypes can be valuable data-gathering tools if they are instrumented for this purpose.
- Anticipate the effect of environment on the system you are designing and the effects of the system on the environment.
- Include the performance analyst on the design team from the beginning; if he/she is perceived as an "outsider," he/she is more likely to be ignored, especially if decisions have already been made.

REFERENCES