MVL-TC Chair Election

Robert Wille (Johannes Kepler University Linz, Austria)

Biosketch

Robert Wille received the Diploma and Dr.-Ing. degrees in computer science from the University of Bremen, Germany, in 2006 and 2009, respectively. From 2006-2015, he was with the Group of Computer Architecture at the University of Bremen and, since 2013, he is with the German Research Center for Artificial Intelligence (DFKI). Additionally, he worked as Lecturer at the University of Applied Science in Bremen, Germany, and as visiting professor at the University of Potsdam, Germany, and the Technical University Dresden, Germany. Since 2015, he is full professor at the Johannes Kepler University of Linz, Austria.

His research interests include the design of circuits and systems for both conventional and emerging technologies. This includes work in the domain of verification and proof engines as well as the synthesis and optimization of quantum circuits, reversible circuits, optical circuits and microfluidic biochips. Since 2007, he published approx. 200 journal and conference papers as well as conducted several research projects (with different academic and industrial partners) in this area. For these works, Robert Wille was awarded the Young Researchers Award from the International Symposium on Multiple-Valued Logic (ISMVL) in 2008 and Best Paper Awards from the Forum on Specification and Design Languages (FDL) in 2010 as well as from the International Conference on Computer-Aided Design in 2013.

Position Statement

Robert Wille served the MVL community in various positions including Reviewer, PC Member, PC Chair, Publicity Chair, and General Chair for several editions of the International Symposium on Multiple-Valued Logic (ISMVL). Since 2008, he participated in each edition of ISMVL and contributed a total of 24 papers in this venue. Besides that, he served as Member at Large of the IEEE Multiple-Valued Logic Technical Committee. For the related Journal of Multiple-Valued Logic and Soft Computing (JMVLSC), he served as guest editor for two special issues. Besides that, he brought MVL topics to a broader attention e.g. by serving as guest editor for a special issue on "Multiple-Valued Logic and Applications" for the Journal on Emerging and Selected Topics in Circuits and Systems (JETCAS). It would be an honor to continue serving the MVL community in general and the IEEE MVL-TC in particular.

For the upcoming term, I see the following main challenges to be addressed by the new MVL-TC Chair:

(1) Increasing the size of the main MVL-TC events (i.e. ISMVL, ULSIWS, RM). To this end, a discussion on broadening the application areas and activities reaching out for addressing neighboring communities shall be conducted. This includes obtaining feedback and suggestions from participants and MVL-TC members identifying related areas as well as contacts which, afterwards, shall be used to update and disseminate Call for Papers, respectively.

(2) Making the community more attractive to young researchers. To this end, new actions, which have been discussed and tested in the recent past (e.g. poster sessions, dedicated travel grants, etc.) should be further developed and strengthened (both conceptually as well as financially).

(3) Improving publication processes and opportunities for research articles beyond conferences and workshops. The current situation for researchers aiming for publishing their MVL-related work is unsatisfactory. The review and publication processes in the mostly relevant journal(s) are often unacceptably long. Here, the process itself shall be re-organized or alternatives shall be explored.