

Keynote Address

Jon Fields, Vice President Design Platforms, Agere Systems



Jon Fields is the Vice President of the Design Platform Organization, Agere Systems, Allentown, Pennsylvania. Jon's team is responsible for IC CAD support, common IP sourcing and design, custom and ASIC IC design kits, new technology introduction, foundry support, IC package design, and Agere's Bangalore development center. Jon joined Agere Systems' predecessor, Bell Laboratories, in 1979. His career has spanned design of 32-bit microprocessors and peripheral chipsets (lead designer of the WE32xxx family's first memory management unit and floating point processor chips), design manager of PC Products, and Sr. Director, Design, Mass Storage Products. Jon has a BSEE from Rutgers University and an M.Eng. from Cornell University.