

TUTORIAL 5: LOW-POWER DESIGN

Presenters:

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Coordinator:

K.S. Murthy, ECIL, Hyderabad, India

Description: The tutorial will describe design considerations for both digital and analog circuits for low power. Various techniques to estimate and optimize powers at all the levels of system design will be described. Interactions among several competing parameters, (that is, cost, performance, weight, and the ease of use), under the constraints of intended application, will be explored. Specific examples from various applications will be used to illustrate the concepts.

K. Roy (Ph.D. Univ. of Illinois, Urbana), is an Asst. Prof. at Purdue Univ. His interests include low-power design, testing, fault tolerance, and FPGA's.

R. Roy (Ph.D. Univ. of Illinois, Urbana), has research interests in testing, synthesis for testability, and low power design.

R. Harjani (Ph.D. Carnegie Mellon Univ.), is an Associate Professor at University of Minnesota.

TUTORIAL 6: C++/JAVA™/UNIX

Coordinator:

Pat Meyer, Sun Microsystems, Mountain View, CA, USA

Description: The tutorial provides an introduction to object-oriented programming in C++, JAVA™, the web-savvy programming language from SUN, and the Internet. JAVA™ has

been touted as "a better C++" and as the way that all new applications will be built. It can be used to build extremely portable tools, as a built-in extension language, as a way to build client-server applications embedded in your Web browser, and in lots of other ways. The tutorial will also cover the role and impact of JAVA™ and the Internet on Electronic Design Automation.

The papers in this program comprise the program of the tenth International Conference on VLSI Design. They reflect the authors opinion and, in the interests of timely dissemination, are published as presented and without change. Their inclusion in this publication does not necessarily constitute endorsement by the conference committee, sponsoring organizations or cooperating societies.