Keynote Speech

Twenty Years of RISC with Lessons for the Future

David R. Ditzel
Transmeta

David Ditzel is president and CEO of Transmeta Corporation, a company that currently has no announced products. He was previously at Sun Microsystems where he was chief scientist for the SPARC Technology Business and director of SPARC Laboratories. He spent nine years at AT&T Bell Laboratories where he was an architect of the CRISP microprocessor. He has published more than three dozen papers in the field of advanced computer design, including co-authoring *The Case for the Reduced Instruction Set Computer*.

Special Lecture

Will Customization Become a Big Factor in Computer Architectures?

Josh Fisher
Hewlett-Packard Labs

Josh Fisher is a Hewlett-Packard Laboratories scientist and currently directs HP Labs' Cambridge, Massachusetts, office. He holds a Ph.D. in computer science from the Courant Institute of Mathematical Sciences at New York University. Before joining HP, he was a computer science professor at Yale University and a founder of Multiflow Computer, which built commercial VLIW computers. In 1984 he was awarded a Presidential Young Investigator award. Fisher has conducted research and advanced development in instruction-level parallelism since 1976.

Monday Lunch Invited Talk

DSP Architecture Tommorrow

Ray Simar
Texas Instruments

Ray Simar is manager of the Advanced DSP Architecture group at Texas Instruments. Most recently, Ray was the chief architect and program manager of the advanced-VLIW TMS320C6x DSP. Prior to the 'C6x, Ray was the chief architect and program manager for the floating-point TMS320C3x and TMS320C4x family of DSPs. In early 1998, Ray was appointed TI Fellow for his contributions to the development of DSP architectures.

Tuesday Lunch Invited Talk

IA-64: Innovations and Futures

Hans Mulder
Intel

Hans Mulder is a principal engineer in the Santa Clara Processor Division. He is responsible for the EPIC technology-based architecture enhancements in IA64 and he is responsible for the performance projections and design support related to all of Intel's IA64 microprocessors under development. He joined Intel in November 1991 as architect in the 64-bit program. Prior to Joining Intel in 1991, he held an academic position at Delft University in the Netherlands. He holds a Ph.D. in electrical engineering from Stanford University.