

Speaker Biographies

Bob Colwell, Intel

Bob Colwell leads the IA32 Architecture Group in a Microprocessor Development Division at Intel's Hillsboro, Oregon facility. Dr. Colwell joined Intel in 1990 as a Senior Architect on the P6 (Pentium Pro) project, and became manager of the Architecture Group two years later. He was named an Intel Fellow in 1997. His previous design experience was as a hardware designer at VLIW pioneer Multiflow Computer, at workstation vendor Perq Systems, and at Bell Labs. Dr. Colwell received his BSEE degree from the University of Pittsburgh in 1977, and his MSEE and PhD from Carnegie Mellon University in 1978 and 1985 respectively.

Michael J. Flynn, Stanford University

Michael J. Flynn is a professor of electrical engineering at Stanford University, Stanford, California. His experience includes 10 years at IBM Corporation working in computer organization and design. He was also a faculty member at Northwestern University and Johns Hopkins University, and the director of Stanford's Computer Systems Laboratory from 1977 to 1983.

Dr. Flynn has served as vice president of the IEEE Computer Society and was founding chairman of CS's Technical Committee on Computer Architecture, as well as ACM's Special Interest Group on Computer Architecture. He was the 1992 recipient of the ACM/IEEE Eckert-Mauchly Award for his contributions to processor classification and computer arithmetic. He was the 1995 recipient of the IEEE-CS Harry Goode Memorial Award in recognition of his outstanding contribution to the design and classification of computer architecture. He is the author of three books and more than 200 technical papers.

Dr. Flynn is a fellow of the IEEE and the ACM.