

PLENARY SESSION I

Tuesday, March 27

8:30am-11:50pm



Session Chair
Ali Iranmanesh
Tavanza



Session Co-Chair
Tak Young
Monterey Design

In this plenary session, four industry leaders deliver their views regarding the critical issues related to electronic design. The session features keynote speeches by Hajime Sasaki (Chairman of the board, NEC), Joe Costello (CEO, think3), Raul Camposano (CTO/GM, Synopsys), Edward Ross (President, TSMC, USA). In the first keynote speech, entitled “Future Platform for Mobile Communication”, Hajime Sasaki will explore three driving forces in the IT revolution that are actualizing an Information Society. The next keynote by Joe Costello will focus on the relationship between quality and profitability. Next, Raul Camposano, addresses various formal techniques for design verification, and explores the “Expanding Use of Formal Techniques in Electronic Design”. Edward Ross will then discuss emerging trends in the EDA, IP, library and design center communities, wherein deep collaboration with foundries is producing a variety of Internet-based solutions that are revolutionizing IC design methodologies. The title of his speech is “IC Design Methodology in the Foundry Era: Introducing ‘Heads-Up’ Design”.