

Message from the Chairs

International Workshop on Compile and Run Time Techniques for Parallel Computing (CRTPC)

Welcome to the ICPP Workshop on Compile/Run Time Techniques for Parallel Computing (CRTPC-04) held in conjunction with the International Conference on Parallel Processing (ICPP-04) in Montreal, Canada, August 15–18, 2004.

It is widely recognized that although the peak performance of parallel systems continues to advance steadily, the sustained realization of high efficiency on parallel systems continues to be a difficult challenge. Achieving high efficiency generally requires explicit attention to considerable detail and the use of message passing, which is very tedious for the development of complex applications. Hence, it is extremely important to develop compile-time and run-time techniques that can ease the burden of the programmers of parallel systems, and help achieve high efficiency.

The workshop features ten papers, from researchers in Asia, Europe, and North America in broad areas of compile and run-time techniques. We thank the authors for their contributions to the workshop, and the referees for their constructive comments.

Vipin Chaudhary, Wayne State University, USA

P. Sadayappan, Ohio State University, USA

Workshop Co-Chairs