

Task Forces in Computer Architecture

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HICSS recognizes that there are a number of important topics in Computer Architecture that could benefit from a focused attention to the issues, leading to (hopefully) consensus as to the direction that work in that topic area ought to take. Task Forces have been organized to deal with the seven topics listed below. Each Task Force will meet during the conference to attempt to understand the issues and reach consensus on what the future direction of the topic area should be. Each Task Force will produce a white paper detailing their recommendations. The set of white papers is expected to be published as a special section of a leading professional magazine.

The following pages contain overview statements and position statements re: the individual Task Forces. These statements will provide the jumping off point for the discussions in their respective meetings.

The Task Forces and their co-ordinators are as follows:

Task Force on Combining General-Purpose and Multimedia in One Package: Challenges and Opportunities

Tom Conte, *NC State University* and Andrew Wolfe, *Princeton University*

Task Force on Configurable Computing Systems

Bill Mangione-Smith, *University of California at Los Angeles*

Task Force on Network Storage Architecture

Garth Gibson, *Carnegie Mellon University*

Task Force on Wireless Networking - Reaching the Extra Mile

Hasan Al-Khatib, *Santa Clara University*

Task Force on Interaction of Architecture and Compilation Technology for High-Performance Single-Processor Design

Pen-Chung Yew and David Lilja, *University of Minnesota*

Task Force on Architectural Trends for Shared-Memory Multiprocessors

Per Stenstrom, *Chalmers University, Sweden*

Task Force on Compiling for Processors with Instruction-Level Parallelism

Michael Schlansker, *Hewlett-Packard Laboratories*