

# Opportunities and Challenges in Building Silicon Products in 65nm and Beyond

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The demand for cheaper, faster and more integrated semiconductor products is expected to drive the scaling of Silicon Technology, enabling continuance of Moore's law at least for another decade. However, technology scaling presents several manufacturing and design technology challenges that must be overcome to build semiconductor products in a cost effective manner. While some of the existing challenges that we face today such as power, process variations are expected to become worse at smaller geometries requiring innovations, new problems may arise in integrating heterogeneous technologies such as RF, MEMS on the same die/package. A myriad of design technology challenges starting from functional validation to timing validation and design for test to manufacturability must be addressed to successfully build products. The talk will highlight challenges and identify some of the opportunities in driving design technologies forward.